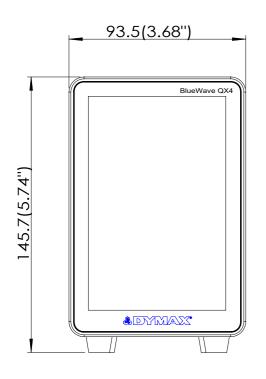
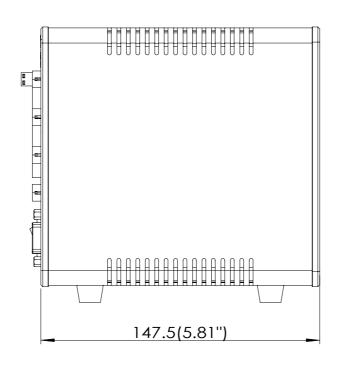
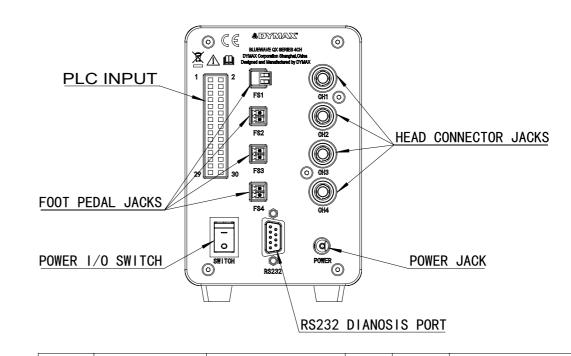
PLC INTERFACE						
	INPUT	OUTPUT				
1	PLC ENABLE	COM	2			
3	EXP0SURE1	LED STATUS 1	4			
5	EXP0SURE2	LED STATUS 2	6			
7	EXPOSURE3	LED STATUS 3	8			
9	EXPOSURE4	LED STATUS 4	10			
11	EXPOSURE ALL	LED STATUS ALL	12			
13	LED INHIBIT 1	INHIBIT STATUS 1	14			
15	LED INHIBIT 2	INHIBIT STATUS 2	16			
17	LED NH B T 3	NH B T STATUS 3	18			
19	LED INHIBIT 4	INHIBIT STATUS 4	20			
21	MASTER INTERLOCK	INTERLOCK STATUS	22			
23	PROG/ANALOG	COM	24			
25	ANALOG NTENS TY	COM	26			
27	INPUT RESERVE1	WARNN I NG	28			
29	COM	OUTPUT RESERVE 1	30			

version	description	issued by	date
A1	initial	Kyo Zhao	







NOTES:

- 1. THE PARTS ARE NOT ALLOWED TO BUMP, SCRATCH OR RUSTDURING ASSEMBLY.
- 2. THE FASTENERS SHOULD BE TIGHTENED ACCRODING TO THE SPECIFIED TORQUE.
- 3. INSPECT AND REMOVE SHARP ANGLES, BURRS AND FOREIGN MATTERAL FROM PARTS BEFORE ASSEMBLY

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1							
ITEM	PN	N	NAME		UNIT	NOTE	
TOLERANCES UNLESS OTHERWISE SPECIFIED		DRAWN Kyo	DATE 2021/10/21		永	YMA	Z ®
DIMENSIONS ARE IN INCHES		CHECKED	DATE		49 L		
FRACTIONAL ONE PLACE TWO PLACE THREE PLACE ANGULAR:	DECIMAL X±.03 DECIMAL XX±.01 E DECIMAL XXX±.005	MANUFACTURING DATE		TITLE:	QX4 V2.0 CONTROLLER		
		Q.A.	DATE				
BREAK	OVE ALL BURRS & K SHARP EDGES R. OR CHAMFER			SIZE	SIZE DWG. NO.		REV
		SCALE: 1:2	SHEET 1 OF 1	A3			A 1